

**ABSTRACT OF THE DISCLOSURE**

The present invention relates to a method for finding the appropriate refresh interval applied for DRAM of the present invent, comprising, detecting startup, providing a clock pulse  
5 as a refresh interval, DRAM self-testing of a plurality of memory cells with the provided refresh interval, modifying the refresh interval and repeating the above steps to find the longest refresh interval as determined by the DRAM self-testing procedure, and using the determined appropriate refresh  
10 interval to refresh the DRAM. Therefore, the appropriate refresh interval, found from the method of the present invention, could be adjusted with the DRAM chip for saving the power with refresh.